



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



SPECIFICATION

MODEL NO. : WLOF00043000WGAAASA00

Summary

4.3 Inch Smart Display Feature

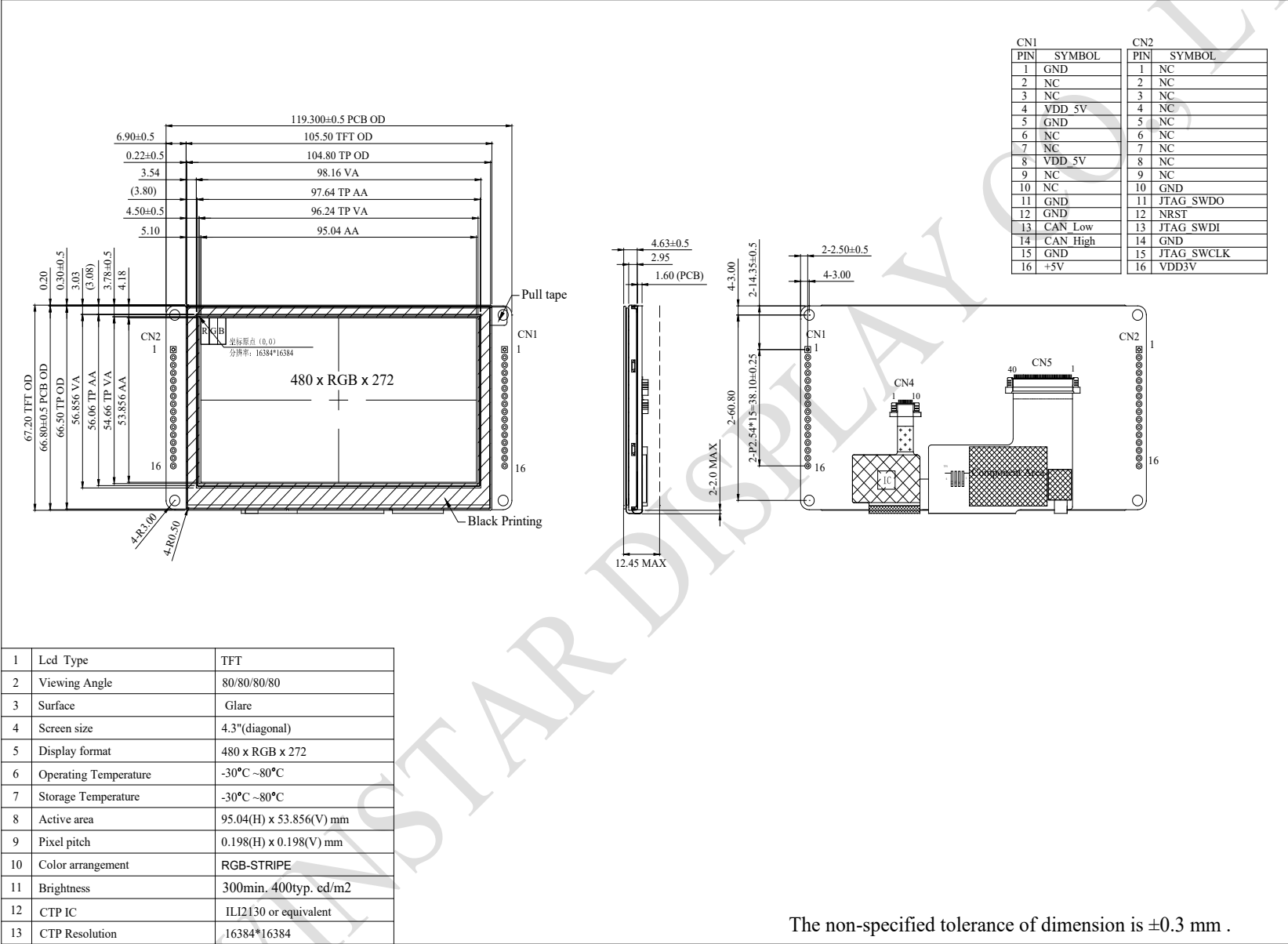
1. DC 5-24V working voltage.
2. Self-testing after booting function.
3. CAN bus communication interface.
4. Supports CANopen protocol, default baud rate at 250Kbps.
5. Built in 16MB flash memory, store the fonts and pictures.
6. Support capacitive touch panel (CTP).
7. Embedded buzzer controlled by Master Device.
8. Demo set HOST can be used on multiple platforms, such as Computer (with USB to CAN Dongle), MCU, Raspberry Pi (with PiCAN2).

Product information

General information

Item	Standard Value	Unit
Operating voltage	5-24	Vdc
Communication Interface	CAN bus	-
MCU	STM32F750	N/A
Flash Memory	16	MB
SDRAM Frequency	108	MHz
LCD display size	4.3	inch
Dot Matrix	480 × RGB × 272(TFT)	dots
Module dimension	119.3(W) × 67.2(H) × 12.45(D)	mm
Active area	95.04(W) × 53.856(H)	mm
Pixel pitch	0.198(H) × 0.198(V)	mm
Brightness	Min: 300; Typ: 400	cd/m ²
LCD type	TFT, Normally Black, Transmissive	
View Direction	80/80/80/80	
Aspect Ratio	16:9	
Touch Panel	Projected Capacitive Touch Panel (PCAP)	
Surface	Glare	

Contour Drawing



Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+80	°C
Storage Temperature	TST	-30	—	+80	°C

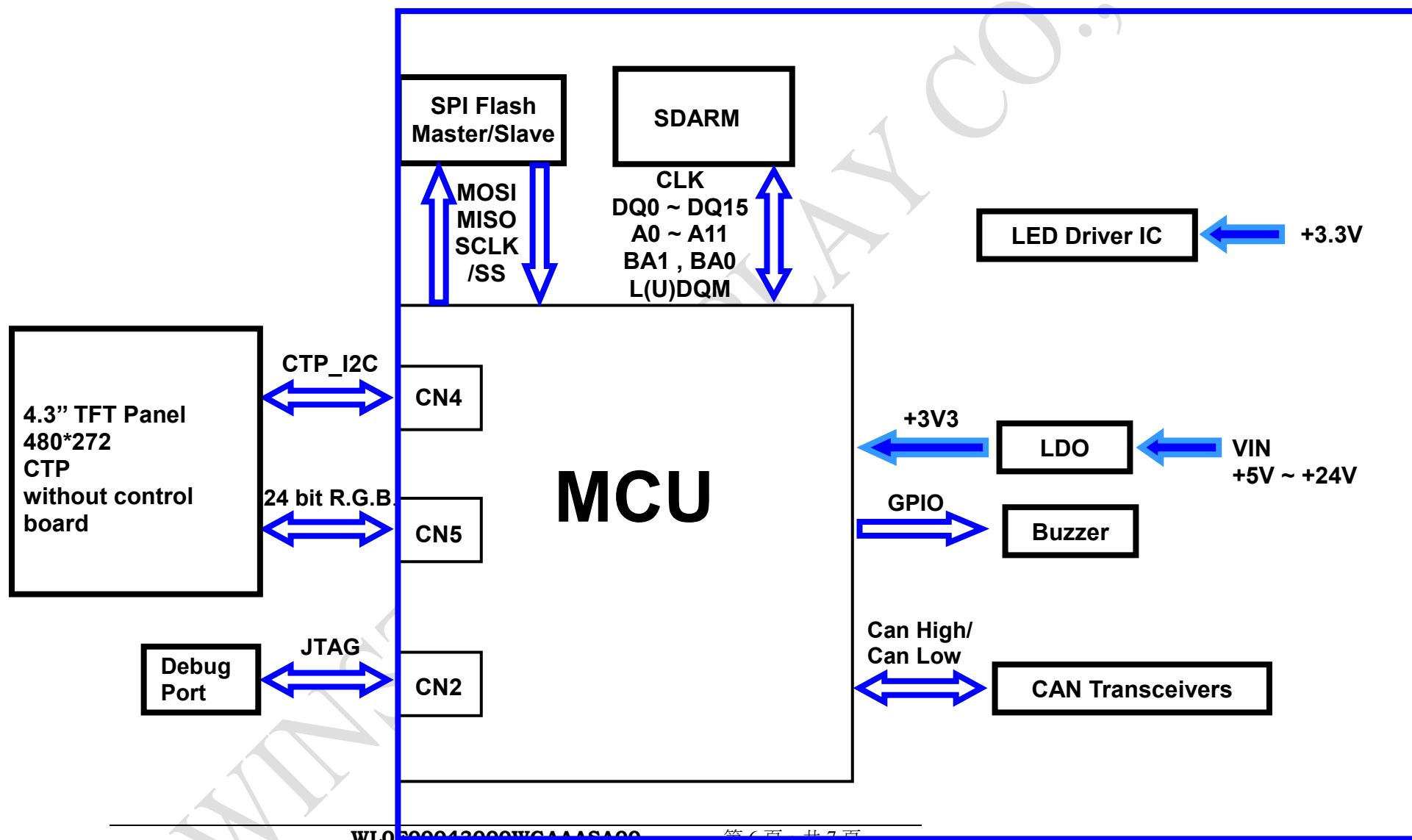
Electrical Characteristics

Item	Symbol	Min	Typ	Max	Unit
Supply Voltage	VCC	5	12	24	V
Supply Current	ICC		170		mA

BOM

Item	Description
LCM	WF43WTWAEDNGA#
PCBA	SV10004R300WA00N0108

Block diagram



Interface

CN1 definition:

Pin	Symbol	Function	Remark
1	GND	GND	Output
2-3	NC	--	--
4	+5V	VIN	Input
5	GND	GND	Output
6-7	NC	--	--
8	+5V	VIN	Input
9-10	NC	--	--
11-12	GND	GND	Output
13	CAN_Low	CAN bus D-	I/O
14	CAN_High	CAN bus D+	I/O
15	GND	Power GND	Input
16	+5V	Power +5V ~ +24V	Input

CN2 definition:

Pin	Symbol	Function	Remark
1-9	NC	--	--
10	GND	GND	Output
11	JTAG_SWDO	Data pin for JTAG interface	I/O
12	NRST	Reset pin for JTAG interface	Input
13	JTAG_SWDI	Data pin for JTAG interface	I/O
14	GND	GND for JTAG interface	Output
15	JTAG_SWCLK	CLK pin for JTAG interface	Input
16	VDD3V	3.3V power for JTAG interface	Output