

TFT DISPLAY SPECIFICATION



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



SPECIFICATION

MODULE NO.: WFN0210A2T0YADNN000

General Specifications

Item	Dimension	Unit
Size	2.1	inch
Dot Matrix	480 x RGB x 480 (TFT)	dots
Module dimension	56.18 x 59.71 x 2.3	mm
Active area	53.28 x 53.28	mm
Pixel pitch	0.111 X 0.111	mm
LCD type	TFT, Normally Black, Transmissive	
Viewing Angle	85/85/85/85	
TFT Interface	3W SPI+RGB18bit	
Backlight Type	LED ,Normally White	
TFT Controller IC	ST7701 or Equivalent	
Touch Panel	Without Touch Panel	
Surface	Anti-Glare	

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Electrical Characteristics

1. Operating conditions

Item		Symbol	Min.	Typ.	Max.	Unit
Power supply Voltage		V _{CI}	2.6	3.0	3.3	V
Input Signal Voltage	Low Level	V _{IL}	0	-	0.3 x V _{CI}	V
	High Level	V _{IH}	0.7 x V _{CI}	-	V _{CI}	V
Power supply Current		I _{VCI}	-	24	35	mA

LED driving conditions

Item	Symbol	Min.	Typ.	Max.	Unit
LED current	-	-	20	25	mA
LED voltage	V _{LED+}	-	12.8	14	V
LED Life Time	-	30000	-	-	Hr

Interface

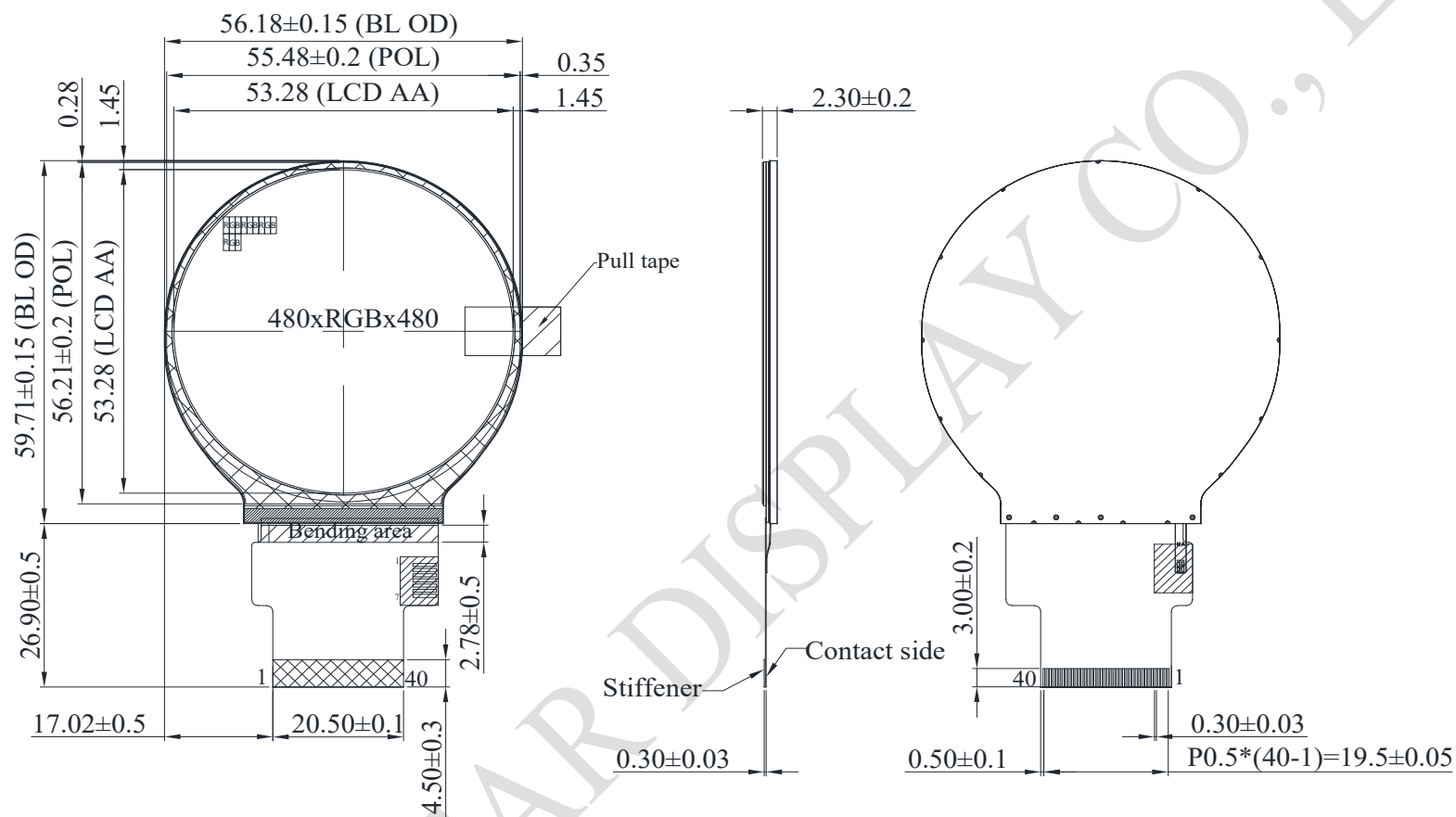
1. LCM PIN Definition

Match connector: XF2M-4015-1A (OMRON) or equivalent.

PIN No	Symbol	I/O	Description
1	VLED+	P	LED ANODE
2	VLED-	P	LED CATHODE
3	VLED-	P	LED CATHODE
4	GND	P	Ground
5	VCI	P	Power supply (VCI=VDD=VCC=VDDI)
6	RESET	I	Reset Signal ,Active Low (RESX)
7	NC	-	No connect.
8	NC	-	No connect.
9	SDA	I	SPI Data signal (In: DIN, Out: DOUT)
10	SCK	I	SPI Clock signal (SCL)
11	CS	I	SPI Chip select signal (CSX)
12	PCLK	I	RGB dot clock signal (DOTCLK)
13	DE	I	RGB data enable signal (ENABLE)
14	VSYNC	I	RGB frame synchronizing signal (VS)
15	HSYNC	I	RGB line synchronizing signal (HS)
16~33	DB0~DB17	I	RGB data signal: DB0:BLUE LSB--DB5:BLUE MSB; DB6:GREEN LSB-- DB11:GREEN,MSB; DB12:RED LSB--DB17:RED MSB.
34	GND	P	Ground
35	NC (TP_INT)	-	No connection (Touch Interrupt)
36	NC(TP_SDA)	-	No connection (Touch IIC Data signal)
37	NC(TP_SCL)	-	No connection (Touch IIC Clock signal)
38	NC(TP_RESET)	-	No connection (Touch Reset Signal)
39	NC(TP_VCI)	-	No connection (Touch Power supply)
40	GND	P	Ground

I : Input, O : Output, P : Power/Ground

Contour Drawing



PIN NO.	SYMBOL
1	VLED+
2	VLED-
3	VLED-
4	GND
5	VCI
6	RESET
7	NC
8	NC
9	SDA
10	SCK
11	CS
12	PCLK
13	DE
14	VSYN
15	HSYN
16~33	DB0~DB17
34	GND
35	NC(TP-INT)
36	NC(TP-SDA)
37	NC(TP-SCL)
38	NC(TP-RESET)
39	NC(TP-VCI)
40	GND

The non-specified tolerance of dimension is ±0.3 mm .