

TFT DISPLAY SPECIFICATION



WINSTAR Display Co.,Ltd.
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SPECIFICATION

MODULE NO.: WF70A2TIFGDBGA#

General Specifications

Item	Dimension	Unit
Size	7.0	inch
Dot Matrix	800 x RGB x 480 (TFT)	dots
Module dimension	165 x 100.0 x 13.7	mm
Active area	154.08 x 85.92	mm
Pixel Pitch	0.1926 x 0.179	mm
LCD type	TFT, Normally White, Transmissive	
Controller IC	SSD1963	
Interface	Digital 8080 family MPU 8bit/16bit	
View Direction	12 o'clock	
Gray Scale Inversion Direction	6 o'clock	
Aspect Ratio	16:9	
Backlight Type	LED, Normally White	
Color arrangement	RGB-STRIPLE	
PCAP IC	ILI2130 or Equivalent	
PCAP Interface	I2C	
PCAP FW Version	0x07.0x00.0x00.0x00.0x65.0x90.0x00.0x01	
PCAP Resolution	16384*16384	
Touch Panel	With PCAP	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Electrical Characteristics

Operating conditions: (CON2.Pin1=GND, Pin2=VDD)

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For LCM	VDD	—	3.0	3.1	3.3	V
Supply Current For LCM	IDD	—	—	220	335	mA

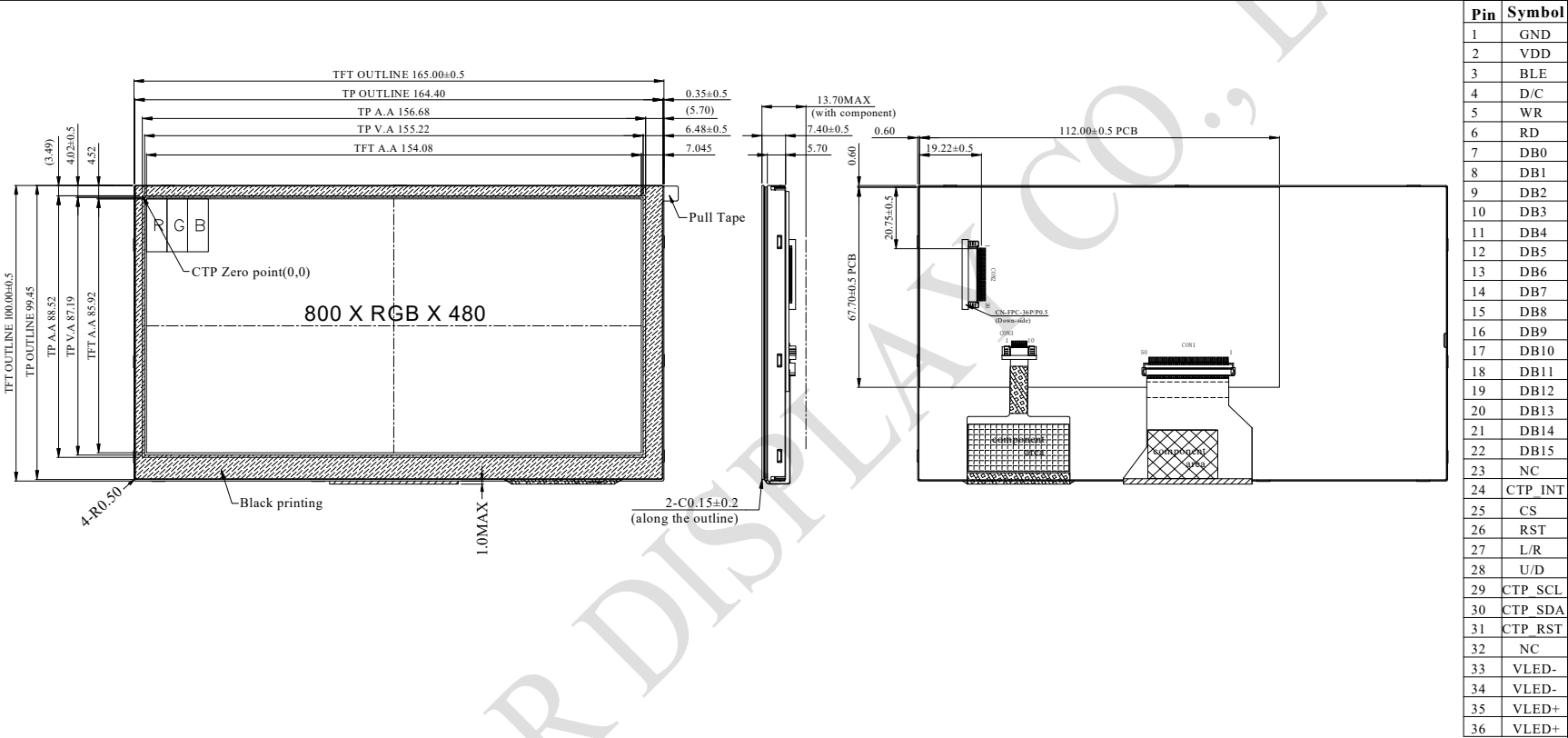
Interface

1. LCM PIN Definition (CON2)

Pin	Symbol	Function
1	GND	System ground pin of the IC .Connect to system ground.
2	VDD	Power Supply : +3.3V
3	BLE	Backlight control signal , H: On \ L: Off
4	D/C	Data/Command select
5	WR	Write strobe signal
6	RD	Read strobe signal
7	DB0	Data bus
8	DB1	Data bus
9	DB2	Data bus
10	DB3	Data bus
11	DB4	Data bus
12	DB5	Data bus
13	DB6	Data bus
14	DB7	Data bus
15	DB8	Data bus (When select 8bits Mode, this pin is NC)
16	DB9	Data bus (When select 8bits Mode, this pin is NC)
17	DB10	Data bus (When select 8bits Mode, this pin is NC)
18	DB11	Data bus (When select 8bits Mode, this pin is NC)
19	DB12	Data bus (When select 8bits Mode, this pin is NC)
20	DB13	Data bus (When select 8bits Mode, this pin is NC)
21	DB14	Data bus (When select 8bits Mode, this pin is NC)
22	DB15	Data bus (When select 8bits Mode, this pin is NC)
23	NC	No connect
24	CTP_INT	CTP_ External interrupt to the host
25	CS	Chip select
26	RST	Hardware reset (RESET)
27	L/R	Left / right selection; Default L/R=H
28	U/D	Up/down selection; ; Default U/D=L

29	CTP_SCL	CTP_SPI Slave mode, chip select, active low / I2C clock input
30	CTP_SDA	CTP_SPI Slave mode, data input / I2C data input and output
31	CTP_RST	CTP_ External Reset, Low is active
32	NC	No connect
33	VLED-	Power for LED Driver IC(GND)
34	VLED-	Power for LED Driver IC(GND)
35	VLED+	Power for LED Driver IC(+5V)
36	VLED+	Power for LED Driver IC(+5V)

Contour Drawing



The non-specified tolerance of dimension is ±0.3 mm .