



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



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SPECIFICATION

MODULE NO.:

WG12232E

General Specification

Item	Dimension	Unit
Number of dots	122 x 32	—
Module dimension	80.0 x 36.0 x 13.7 (MAX)	mm
View area	60.0 x 18.0	mm
Active area	53.64 x 15.64	mm
Dot size	0.40 x 0.45	mm
Dot pitch	0.44 x 0.49	mm
Duty	1/32	
Backlight Type	LED	
IC	RW1020	
Interface	6800	

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	°C
Storage Temperature	T_{ST}	-30	—	+80	°C
Input Voltage	V_{IN}	-0.3	—	$V_{DD}+0.3$	V
Supply Voltage For Logic	$V_{DD}-V_{SS}$	-0.3	—	7.0	V

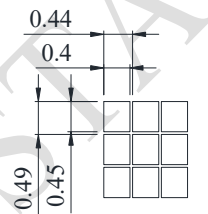
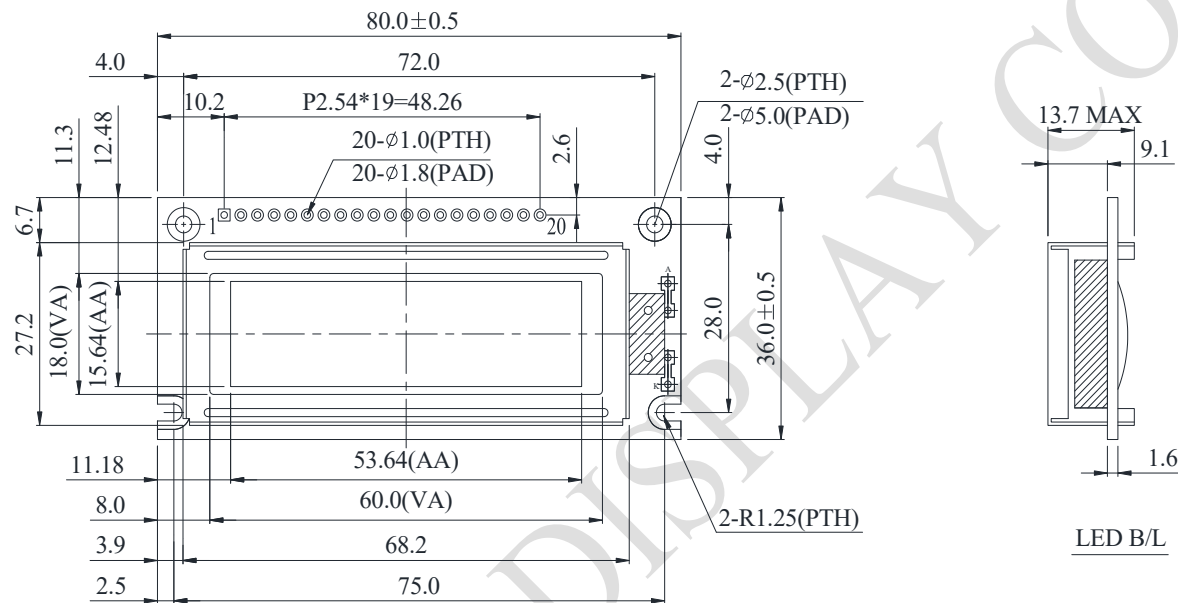
Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	4.5	5.0	5.5	V
Supply Voltage For LCD	$V_{DD}-V_0$	$T_a=-20^{\circ}\text{C}$	—	—	5.8	V
		$T_a=25^{\circ}\text{C}$	4.2	—	4.5	V
		$T_a=+70^{\circ}\text{C}$	3.9	—	—	V
Input High Volt.	V_{IH}	—	$0.85V_{DD}$	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	0	—	0.8	V
Output High Volt.	V_{OH}	—	$0.8V_{DD}$	—	V_{DD}	V
Output Low Volt.	V_{OL}	—	0	—	0.4	V
Supply Current	I_{DD}	$V_{DD}=5.0\text{V}$	—	0.39	1.0	mA

Interface Pin Function

Pin No.	Symbol	Level	Description
1	V _{SS}	0V	GND
2	V _{DD}	5V	Power supply for logic
3	V _O	(Variable)	Contrast Adjustment
4	A0	H/L	H : Data L : Instruction
5	CS1	H/L	Chip select signal for IC1
6	CS2	H/L	Chip select signal for IC2
7	CL	—	External clock 2KHZ
8	E	H/L	Enable Signal
9	R/ W	H/L	H : Read data; L : Write data
10	DB0	H/L	Data bus line
11	DB1	H/L	Data bus line
12	DB2	H/L	Data bus line
13	DB3	H/L	Data bus line
14	DB4	H/L	Data bus line
15	DB5	H/L	Data bus line
16	DB6	H/L	Data bus line
17	DB7	H/L	Data bus line
18	/RES	H/L	Reset the LCM
19	A	—	Power supply for B/L +
20	K	—	Power supply for B/L -

Contour Drawing



DOT SIZE
SCALE 10/1

The non-specified tolerance of dimension is $\pm 0.3\text{mm}$.

PIN NO.	SYMBOL
1	V _{ss}
2	V _{dd}
3	V _o
4	A0
5	CS1
6	CS2
7	CL
8	E
9	R/ $\bar{W}$
10	DB0
11	DB1
12	DB2
13	DB3
14	DB4
15	DB5
16	DB6
17	DB7
18	$\overline{\text{RES}}$
19	A
20	K