

TFT DISPLAY SPECIFICATION



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SPECIFICATION

MODULE NO.: WF43WTWAESNN0#

General Specifications

Item	Dimension	Unit
Size	4.3	inch
Dot Matrix	480 x RGBx272(TFT)	dots
Module dimension	105.5(W) x 67.2(H) x 2.95(D)	mm
Active area	95.04 x 53.856	mm
Pixel pitch	0.198 x 0.198	mm
LCD type	TFT, Normally Black, Transmissive	
Viewing Angle	80/80/80/80	
Aspect Ratio	16:9	
TFT Driver IC	NV3041A-01 or equivalent	
TFT Interface	4 Line data QSPI	
Backlight Type	LED, Normally White	
Touch Panel	Without Touch Panel	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+80	°C
Storage Temperature	TST	-30	—	+80	°C

Electrical Characteristics

1. Operating conditions:

Item	Symbol	Min	Typ	Max	Unit
Power supply for analog circuit	VCI	3.0	3.3	3.6	V
Power supply for logic circuit	IOVCC	1.65	1.8	3.3	V
Current for Driver	I _{VCI}	-	30	45	mA
	I _{IOVCC}	-	20	30	mA

2. LED driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit
LED current	-	-	20	-	mA
Power Consumption	-	540	640	680	mW
LED voltage	V _{LED+}	27	32	34	V
LED Life Time	-	50,000	-	-	Hr

Interface

1. LCM PIN Definition

Pin	Symbol	Function
1	VLED-	Power for LED backlight cathode
2	VLED+	Power for LED backlight anode
3	NC	No connection
4	VCI	Power supply for analog circuit
5	IOVCC	Power supply for digital interface I/O pins
6	GND	Ground
7	RESET	Reset.
8	GND	Ground
9	CS	Chip select, internal pull high. (CSX)
10	SCL	This pin is used serial interface clock
11	GND	Ground
12	SDO1	SPI bi-direction data pin (SDA)
13	SDO2	Std-SPI(4 wire): command or parameter select. Quard-SPI: the second data pin (DCX)
14	SDO3	The third pin of Quard-SPI (DB[2])
15	SDO4	The fourth pin of Quard-SPI (DB[3])
16	GND	Ground
17	TE	Tearing effect output pin is used to synchronize MCU frame writing, activated by S/W command. When this pin is not activated (TE function OFF), this pin is GND level.
18	GND	Ground

Contour Drawing

