WINSTAR Display

OLED SPECIFICATION

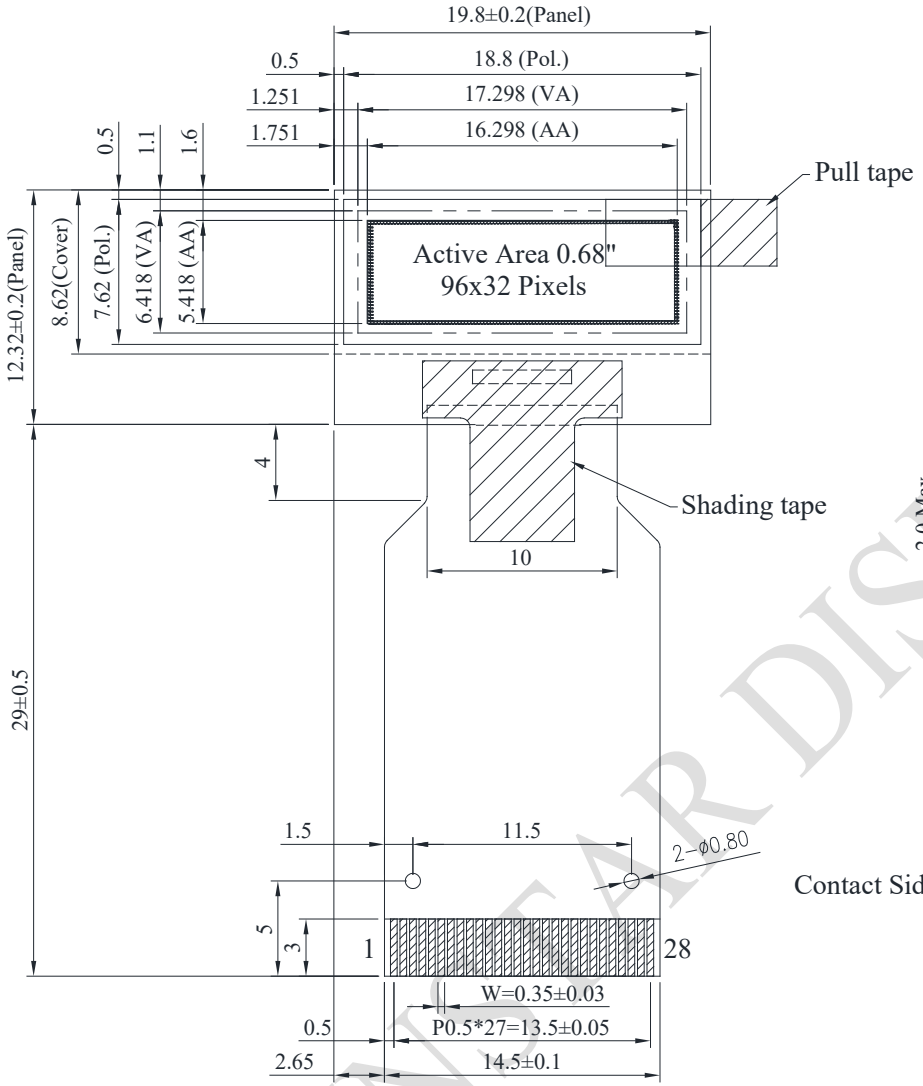
Model No:

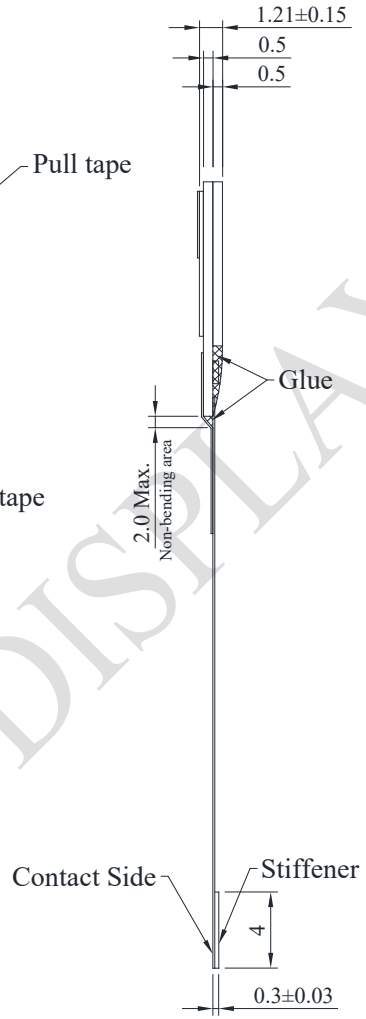
WEO009632C (ZIF FPC)

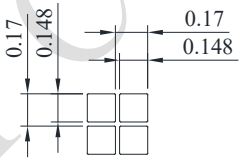
General Specification

Item	Dimension	Unit
Dot Matrix	96 x 32 Dots	—
Module dimension	19.80 x 12.32 x 1.21	mm
Active Area	16.298 x 5.418	mm
Pixel Size	0.148 x 0.148	mm
Pixel Pitch	0.17 x 0.17	mm
Display Mode	Passive Matrix	
Display Color	Monochrome	
Drive Duty	1/32 Duty	
IC	SSD1315	
Interface	6800, 8080,4-Wire SPI, I2C	
Size	0.68 inch	

Contour Drawing & Block Diagram

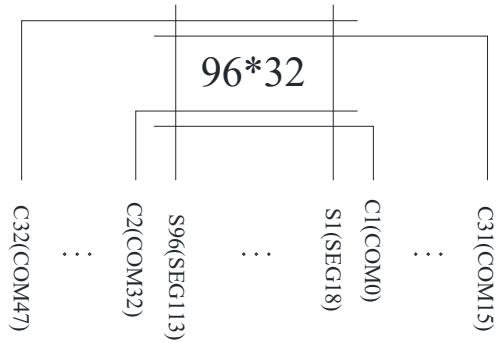






Dots Size
Scale 10/1

PIN	SYMBOL	PIN	SYMBOL
1	VSS	15	D/C#
2	NC	16	WR#
3	VDD	17	E/RD#
4	NC	18	D0
5	NC	19	D1
6	NC	20	D2
7	NC	21	D3
8	NC	22	D4
9	VDD	23	D5
10	BS1	24	D6
11	BS2	25	D7
12	NC	26	IREF
13	CS#	27	VCOMH
14	RES#	28	VCC



Seg&Com layout

The non-specified tolerance of dimension is ±0.3 mm .

Interface Pin Function

<table>
<tr><th>No.</th><th>Symbol</th><th>Function</th></tr>
<tr><td>1</td><td>VSS</td><td>Ground pin. It must be connected to external ground.</td></tr>
<tr><td>2</td><td>NC</td><td>No connection</td></tr>
<tr><td>3</td><td>VDD</td><td>Power supply pin for core logic operation.</td></tr>
<tr><td>4~8</td><td>NC</td><td>No connection</td></tr>
<tr><td>9</td><td>VDD</td><td>Power supply pin for core logic operation.</td></tr>
<tr><td>10</td><td>BS1</td><td rowspan="2">MCU bus interface selection pins. Select appropriate logic setting as described in the following table. BS2, BS1 are pin select
<table>
<tr><th>BS[2:1]</th><th>Interface</th></tr>
<tr><td>00</td><td>4 line SPI</td></tr>
<tr><td>01</td><td>I2C</td></tr>
<tr><td>11</td><td>8-bit 8080 parallel</td></tr>
<tr><td>10</td><td>8-bit 6800 parallel</td></tr>
</table>
Note
(1) 0 is connected to VSS
(2) 1 is connected to VDD</td></tr>
<tr><td>11</td><td>BS2</td></tr>
<tr><td>12</td><td>NC</td><td>No connection</td></tr>
<tr><td>13</td><td>CS#</td><td>This pin is the chip select input connecting to the MCU.
The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW).</td></tr>
<tr><td>14</td><td>RES#</td><td>This pin is reset signal input. When the pin is pulled LOW, initialization of the chip is executed. Keep this pin HIGH (i.e. connect to VDD) during normal operation.</td></tr>
<tr><td>15</td><td>D/C#</td><td>This pin is Data/Command control pin connecting to the MCU.
When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data.
When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register.
In I2C mode, this pin acts as SA0 for slave address selection.</td></tr>
<tr><td>16</td><td>W/R#</td><td>This is read / write control input pin connecting to the MCU interface.
When interfacing to a 6800-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this pin is pulled HIGH (i.e. connect to VDD) and write mode when LOW.
When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled LOW and the chip is selected.
When serial or I2C interface is selected, this pin must be connected to VSS.</td></tr>
<tr><td>17</td><td>E/RD#</td><td>This pin is MCU interface input.
When 6800 interface mode is selected, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled HIGH and the chip is selected.
When 8080 interface mode is selected, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected.
When serial or I2C interface is selected, this pin must be connected to VSS.</td></tr>
</table>

18	D0	These are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SDIN. When I2C mode is selected, D2, D1 should be tied together and serve as SDAout, SDAin in application and D0 is the serial clock input, SCL.
19	D1	
20	D2	
21	D3	
22	D4	
23	D5	
24	D6	
25	D7	
26	IREF	This is segment output current reference pin. When external IREF is used, a resistor should be connected between this pin and VSS to maintain the IREF current at 30uA.
27	VCOMH	COM signal deselected voltage level. A capacitor should be connected between this pin and VSS.
28	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin.

Absolute Maximum Ratings

Parameter	Symbol	Min	Typ.	Max	Unit
Supply Voltage for Logic	VDD	-0.3	-	4	V
Supply Voltage for Display	VCC	0	-	18	V
Operating Temperature	TOP	-40	-	+80	°C
Storage Temperature	TSTG	-40	-	+85	°C

Electrical Characteristics

1 DC Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD	—	1.65	3.0	3.3	V
Supply Voltage for Display	VCC	—	7.5	12.0	12.5	V
High Level Input	VIH	—	0.8×VDD	—	VDD	V
Low Level Input	VIL	—	0	—	0.2×VDD	V
High Level Output	VOH	Iout = 100uA	0.9×VDD	—	VDD	V
Low Level Output	VOL	Iout = 100uA	0	—	0.1×VDD	V
Display 50% Pixel on	ICC	VCC=12V	—	5.0	7.5	mA